

	Application No.	Applicant(s)	
	09/524,901	QI ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Daniel D. Chang	2819	
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	opears on the cover sheet will IS (OR REMAINS) CLOSED in S5) or other appropriate community RIGHTS. This application is s	n this application. If not included unication will be mailed in due course. THIS	S itive
1. X This communication is responsive to Amendment filed 3	<u>lune 1, 2004</u> .		
2. 🔀 The allowed claim(s) is/are <u>1-7 and 9-22</u> .			
3. The drawings filed on are accepted by the Exami	iner.	and the second	
4. Acknowledgment is made of a claim for foreign priority a) All b) Some* c) None of the: 1. Certified copies of the priority documents had 2. Certified copies of the priority documents had 3. Copies of the certified copies of the priority International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DAT noted below. Failure to timely comply will result in ABANDO THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ave been received. ave been received in Application documents have been received E" of this communication to file	n No d in this national stage application from the	
5. A SUBSTITUTE OATH OR DECLARATION must be sul INFORMAL PATENT APPLICATION (PTO-152) which g			
6. CORRECTED DRAWINGS (as "replacement sheets") n (a) ☐ including changes required by the Notice of Draftsp 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☒ including changes required by the attached Examin Paper No./Mail Date Identifying indicia such as the application number (see 37 CFI each sheet. Replacement sheet(s) should be labeled as such if 7. ☐ DEPOSIT OF and/or INFORMATION about the de attached Examiner's comment regarding REQUIREMENT.	erson's Patent Drawing Review er's Amendment / Comment or R 1.84(c)) should be written on the the header according to 37 CF posit of BIOLOGICAL MATI	in the Office action of the drawings in the front (not the back) of R 1.121(d). ERIAL must be submitted. Note the	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-94) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SI Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Depos of Biological Material	8) 6. ☐ Interview S Paper No./ B/08), 7. ☒ Examiner's	formal Patent Application (PTO-152) ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance -	
		Daniel D. Chang Primary Examiner Art Unit: 2819	

Acknowledgement

Receipt is acknowledged of the Amendment filed June 1, 2004.

Drawings

The drawings are objected to as failing to comply with 37 CFR 1.84(i) & 37 CFR 1.84(p) because lines, numbers & letters are not uniformly thick and well defined, clean, durable, and black (poor line quality). Also, some characters are too small (see figures 3 and 4).

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Eustace Isidore on July 7, 2004.

The application has been amended as follows:

Claim 13, lines 10 and 13, "first transistor" has been deleted and replaced by --first-type transistor--.

Claim 13, lines 11, 12, 14, and 15, "second transistor" has been deleted and replaced by --second-type transistor--.

Application/Control Number: 09/524,901

Art Unit: 2819

On page 6 of the amendment filed June 1, 2004, the second occurring claim 13 which states "13. (canceled)" has been deleted.

Claim 17, line 2, "first transistor" has been deleted and replaced by --first-type transistor-

Claim 17, line 6, "second transistor" has been deleted and replaced by --second-type transistor--.

Claim 19, lines 2-3, "said plurality of transistors each have a control node and a first and second data node and wherein" has been deleted.

Claim 19, line 3, "said" after "controls" has been deleted and replaced by --a--.

Claim 20, line 1, "12" has been deleted and replaced by --13--.

Claim 21, lines 2-3, "said plurality of transistors each have a control node and a first and second data node and wherein" has been deleted.

Claim 21, line 3, "said" after "controls" has been deleted and replaced by --a--.

Claim 22, line 1, "20" has been deleted and replaced by --12--.

Reasons for Allowance

Claims 1-7 and 9-22 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Yamashita et al., taken alone or in combination of other references, does not teach or fairly suggest a high speed static multiplexer comprising, among other things, a pull-up circuit which includes a first-type transistor and a plurality of second-type transistors, each having a control node, and a first and second data node, wherein said second data node of each of said plurality of second-type transistors is coupled to the control node of said first-type transistor, said

Application/Control Number: 09/524,901

Art Unit: 2819

first data node of each of said plurality of second-type transistors is coupled to a respective one of said plurality of data inputs, and said control node of each of said plurality of second-type transistors is coupled to one of said at least one select input, as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Application/Control Number: 09/524,901

Art Unit: 2819

Daniel D. Chang

Page 5

Daniel D. Chang Primary Examiner Art Unit 2819

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